

- ★ Super Low Gate Charge
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary



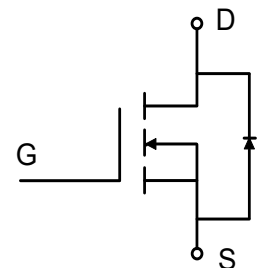
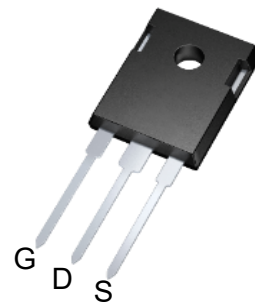
BVDSS	RDSON	ID
200V	20 mΩ	100A

Description

The UD100N20H is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The UD100N20H meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

TO247 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	200	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	100	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	63	A
I_{DM}	Pulsed Drain Current ²	400	A
EAS	Single Pulse Avalanche Energy ³	1190	mJ
I_{AS}	Avalanche Current	---	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	272	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	---	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	0.46	$^\circ C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	200	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	---	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =61A	---	20	25.5	mΩ
		V _{GS} =4.5V, I _D =61A	---	---	---	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	3	---	5	V
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient		---	---	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =150V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =150V, V _{GS} =0V, T _J =100°C	---	---	100	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =31A	---	73	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.2	---	Ω
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =10V, I _D =61A	---	134	---	nC
Q _{gs}	Gate-Source Charge		---	49.6	---	
Q _{gd}	Gate-Drain Charge		---	39.6	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} = 50V, R _L =1.25Ω R _G = 3Ω, V _{GS} =10V	---	36.3	---	ns
T _r	Rise Time		---	9.2	---	
T _{d(off)}	Turn-Off Delay Time		---	64	---	
T _f	Fall Time		---	6.3	---	
C _{iss}	Input Capacitance	V _{DS} =50V, V _{GS} =0V, f=1MHz	---	8826	---	pF
C _{oss}	Output Capacitance		---	532	---	
C _{rss}	Reverse Transfer Capacitance		---	148	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	65	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =31A, T _J =25°C	---	---	1.2	V
t _{rr}	Reverse Recovery Time	I _F =61A, di/dt=100A/μs, T _J = 25 °C	---	102	---	nS
Q _{rr}	Reverse Recovery Charge		---	550.3	---	nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

Notes 2.EAS condition: T_J=25°C, V_{DD}=40V, V_G=10V, R_G=25Ω, L=0.5mH.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

Typical Electrical And Thermal Characteristics (Curves)

Figure 1. Output Characteristics

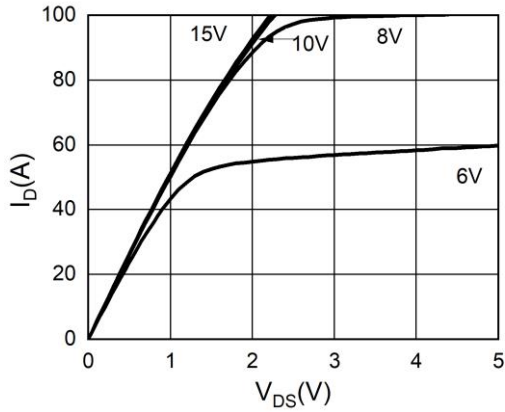


Figure 2. Transfer Characteristics

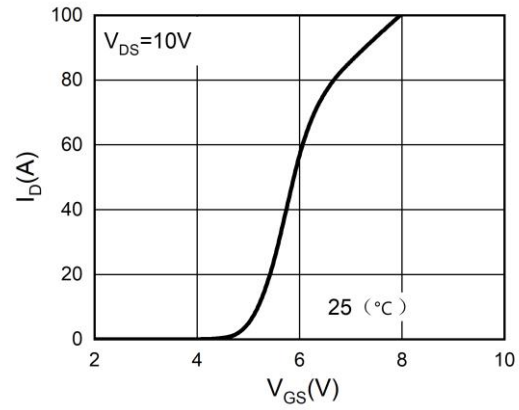


Figure 3. Power Dissipation

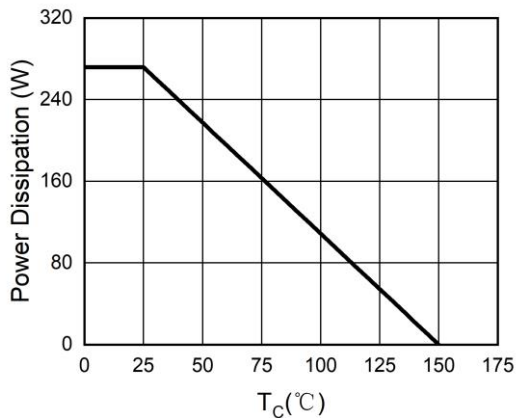


Figure 4. Drain Current

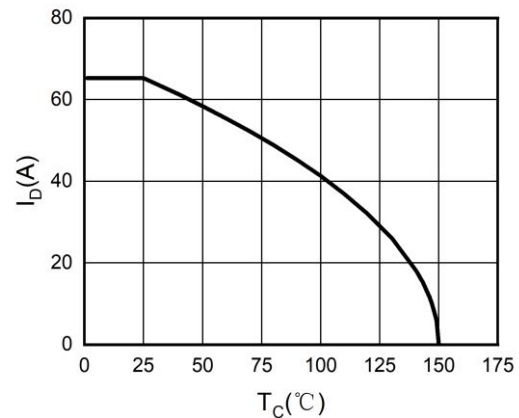


Figure 5. BV_{DSS} vs Junction Temperature

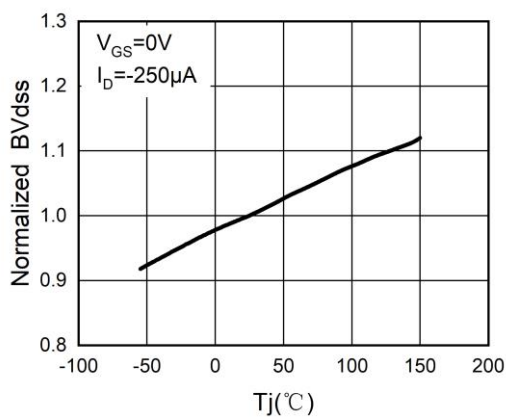
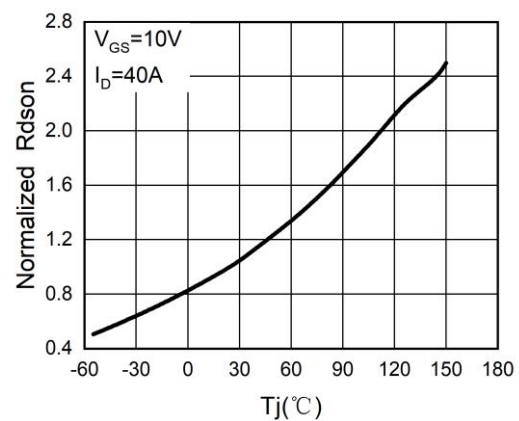


Figure 6. $R_{DS(ON)}$ vs Junction Temperature



Typical Electrical And Thermal Characteristics (Curves)

Figure 7. Gate Charge Waveforms

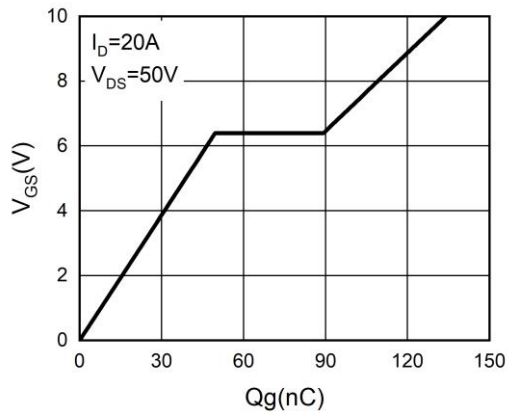


Figure 8. Capacitance

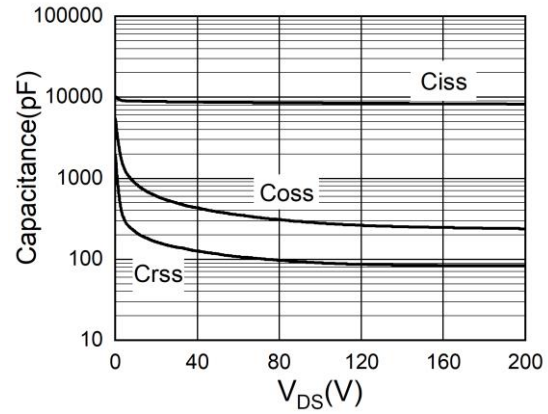


Figure 9. Body-Diode Characteristics

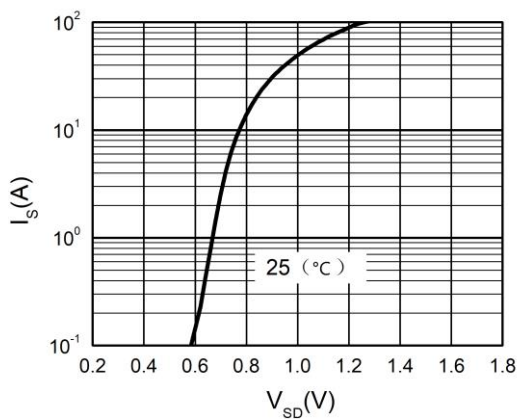
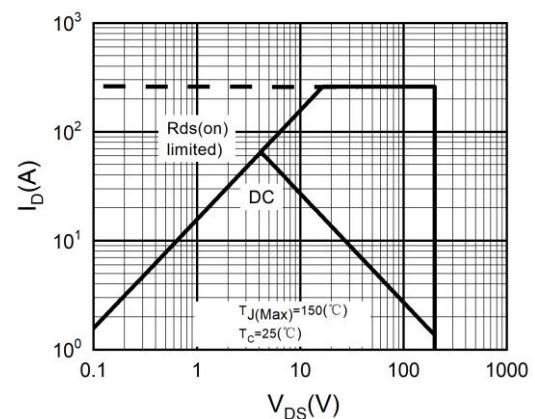
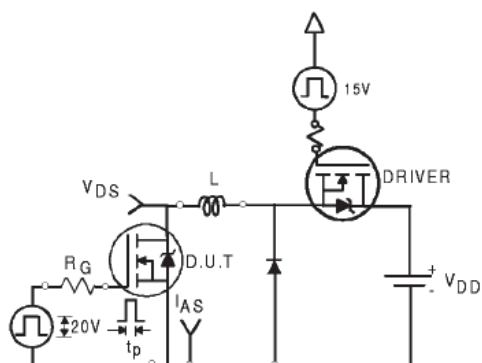


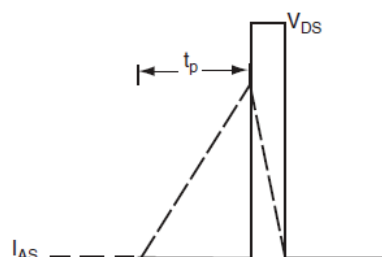
Figure 10. Maximum Safe Operating Area



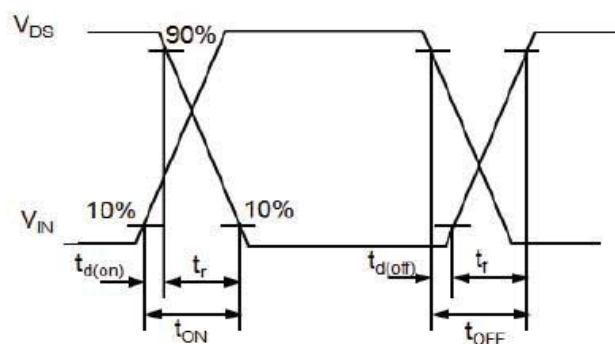
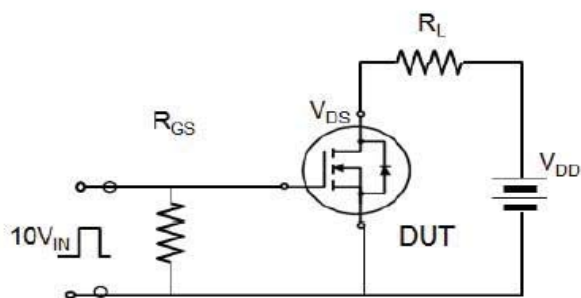
Avalanche Test Circuit



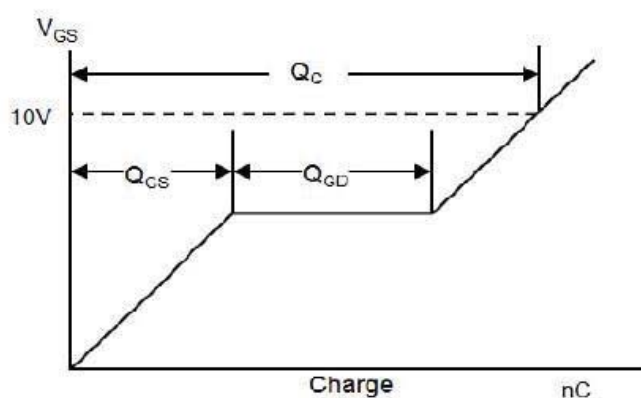
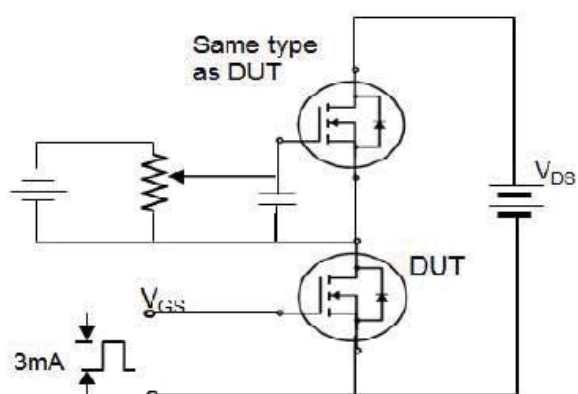
$$E_{AS} = -\frac{1}{2} L I_{AS}^2$$



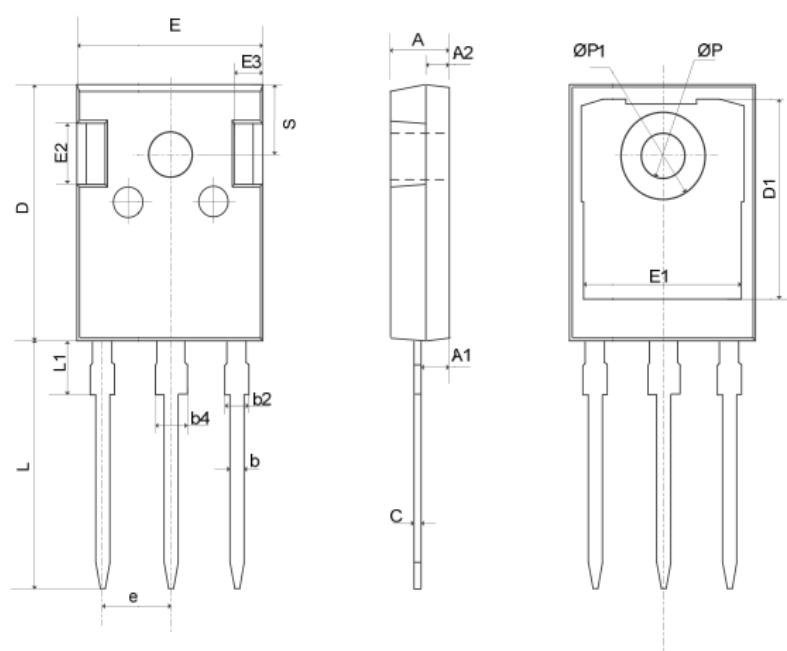
Switching Time Test Circuit



Gate Charge Test Circuit



Mechanical Dimensions for TO-247



COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	4.80	5.20
A1	2.21	2.61
A2	1.85	2.15
b	1.11	1.36
b2	1.91	2.21
b4	2.91	3.21
c	0.51	0.75
D	20.70	21.30
D1	16.25	16.85
E	15.50	16.10
E1	13.00	13.60
E2	4.80	5.20
E3	2.30	2.70
e	5.44BSC	
L	19.62	20.22
L1	—	4.30
ØP	3.40	3.80
ØP1	—	7.30
S	6.15BSC	